

## IN THE UNITED STATES

3/ A+ Electro T. YWN9 6.25-G7

PATENT AND TRADEMARK OFFICE

APPLICANT(S):

Nalini Ranjan and Xiaoyi Guo

SERIAL NO.:

09/057.047

FILING DATE:

08 April 1998

TITLE:

AGP/DDR INTERFACES FOR FULL SWING AND REDUCED SWING

(SSTL) SIGNALS ON AN INTEGRATED CIRCUIT CHIP

**EXAMINER:** 

Don Le

**GROUP ART UNIT:** 

2819

RECEIVED

ATTY. DKT. NO.:

3156 US

JUN 25 1999

**TECHNOLOGY CENTER 2800** 

## **CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner For Patents, Washington, D.C. 20231, on the date shown below:

Dated:

Greg T. Sueoka, Reg. No.: 33,800

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, DC 20231

## RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

In response to the Office Action dated June 3, 1999, please amend the application as

follows:

IN THE CLAIMS:

Please cancel claims 16/21